FIG. 1

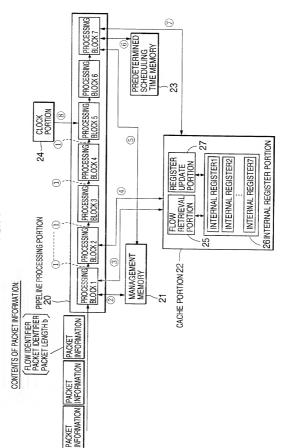
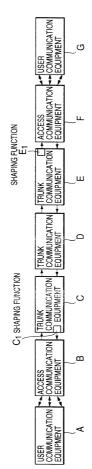
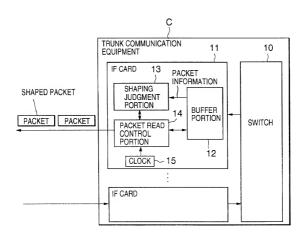


FIG.2

NETWORK MODEL





CONTENTS OF SIGNALS BETWEEN BLOCKS

- SIGNALS BETWEEN PROCESSING BLOCKS IN PIPELINE PROCESSING PORTION FLOW IDENTIFIER, PACKER IDENTIFIER, TK, L, P,PT; Bit, X, Y, Z, W, fbit, Y2 AS INTERNAL CONVERSION
- PROCESSING BLOCK 1 → FLOW IDENTIFIER IN CACHE PORTION , CACHE PORTION → K, B IN PROCESSING BLOCK 1 FLOW IDENTIFIER AS MANAGEMENT MEMORY ADDRESS; TK, L, P, RT AS DATA (17)
- ④ PROCESSING BLOCK 2 → FLOW IDENTIFIER, PACKET LENGTH b IN CACHE PORTION
- FLOW IDENTIFIER AS MANAGEMENT MEMORY ADDRESS, TOKEN ADDED VALUE TK, TOKEN ADDITION INTERVALL AS DATA, P = W AS NEW TOKEN VALUE, AND RT = Z AS NEW PREDETERMINED SCHEDULING TIME
 - © PREDETERMINED SCHEDULING TIME Z, PACKET IDENTIFIER
- PROCESSING BLOCK 7 → FLOW IDENTIFIER, PACKET LENGTH b IN CACHE PORTION
- ® CURRENT TIME NT

FIG.5(A)

FIELD CONFIGURATION	N FOR MANAGEME	NT MEMORY	
TOKEN	TOKEN	TOKEN VALUE P	LATEST
ADDED	ADDITION		SCHEDULING
VALUE TK	INTERVAL L		TIME RT

FIG.5(B)

FIELD CON	FIGURATION FOR INT	ERNAL REGISTER	
VALID-	FLOW	NUMBER OF	PACKET LENGTH
BIT	IDENTIFIER	PACKETS K	SUM TOTAL B

FIG.5(C)

FIELD CONFIGURATION FOR PREDETERMINED	SCHEDULING TIME MEMORY
PREDETERMINED SCHEDULING TIME	PACKET IDENTIFIER

PROCESSING FLOW OF PIPELINE PROCESSING PORTION

PROCESSING BLOCK 1

ASSIGN FLOW IDENTIFIER OF INPUT
PACKET TO MANAGEMENT MEMORY
AS ADDRESS AND OBTAIN TOKEN ADDED
VALUE TK, TOKEN ADDITION INTERVAL L,
TOKEN VALUE P, AND LATEST
SCHEDULING TIME RT OF RELEVANT FLOW,
ASSIGN FLOW IDENTIFIER OF INPUT PACKET
TO CACHE PORTION AND OBTAIN
NUMBER OF PACKETS K AND PACKE LENGTH
SUM TOTAL B IN POPELINE PROCESSING
PORTION OF RELEVANT FLOW AS RETRIEVAL
RESULT IN CACHE PORTION.

PROCESSING BLOCK 2

 WHEN K = 0, CALCULATE X = P - b, AND fbit = 1 if X > 0 fbit = 0 if X ≤ 0.

 WHEN K > 0, CALCULATE X = P - (B + b), AND fbit = 1 if X > 0 fbit = 0 if X ≤ 0.

3) SEND FLOW IDENTIFIER AND PACKET LENGTH 6 TO CACHE PORTION

PROCESSING BLOCK 3

WHEN fbit = 1, CALCULATE Y = 1/TK.
WHEN fbit = 0, CALCULATE Y = (| x | + 1) /TK.

PROCESSING BLOCK 4

z = Y x L + RT, w = X x 1 (fbit = 1), w = 0 (fbit = 0)

PROCESSING BLOCK 5

WHEN NT > Z, f2bit = 0. WHEN NT \leq Z, CALCULATE Y2 =(NT - 2)/TK, AND f2bit = 1

PROCESSING BLOCK 6

WHEN f2bit = 1, Z = NT, AND CALCULATE W = W + NT - Z.

PROCESSING BLOCK 7

- REGISTER Z AND PACKET IDENTIFIER IN PREDETERMINED SCHEDULING TIME MEMORY.
 WRITE TK, L, W, Z TO MANAGEMENT MEMORY USING FLOW IDENTIFIER AS ADDRESS.
- SEND FLOW IDENTIFIER AND 6 TO CACHE PORTION.

RECEIVE A FLOW IDENTIFIER FROM PROCESSING BLOCK 1 AND RETRIEVE

PROCESSING OUTLINE OF CACHE PORTION

THE FLOW IDENTIFIER REGISTERED
IN AN INTERNAL REGISTER.
IF REGISTERED, RETURN THE NUMBER
OF PACKETS K AND THE SUM TOTAL B
OF A PACKET LENGTH CONTAINED IN
THE RELEVANT INTERNAL REGISTER
TO THE PROCESSING BLOCK 1. IF NOT
REGISTERED RETURN K = 2.

RECEIVE A FLOW IDENTIFIER flowinfo1 AND A PACKET LENGTH pktlen1 FROM PROCESSING BLOCK 2 AND RECEIVE A FLOW IDENTIFIER flowinfo2 AND A PACKET LENGTH pktlen2 FROM PROCESSING BLOCK 7.

- WHEN flowinfo1= flowinfo2, RETRIEVE AN INTERNAL REGISTER HAVING THE FLOW IDENTIFIER OF flowinfo1 AND UPDATE THE NUMBER OF PACKETS K = K AND THE PACKET LENGTH SUM TOTAL
- B = B + pktlen1 pktlen2. ■WHEN flowinfo1≠ flowinfo2:
- ① PROCESSING CONCERNING PROCESSING BLOCK 2

BEOGNE AN INTERNAL REGISTER HAVING THE FLOW IDENTIFIER OF flowinfol AND UPDATE K = K + 1 AND B = B + pktlen1. IF THE RELEVANT INTERNAL REGISTER IS NOT PROVIDED, NEWLY REGISTER K = 1, valid-bit on, AND B = pktlen1

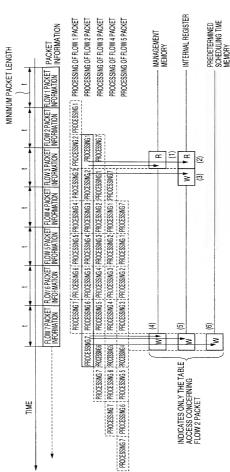
© PROCESSING CONCERNING PROCESSING BLOCK 7

RETRIEVE AN INTERNAL REGISTER HAVING THE FLOW IDENTIFIER OF Ideninfoz AND UPDATE K = K - 1 AND B = B - pktlen2. IF K = 1 IS FIRST RETRIEVED FROM THE RELEVANT INTERNAL REGISTER, UPDATE ONLY valid - bit = off.

EXPLANATION OF VARIABLES:

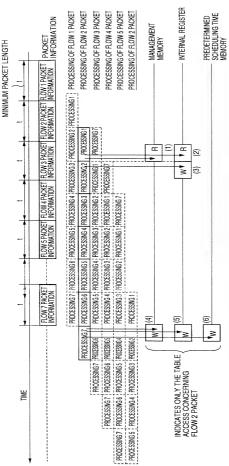
- READ FROM MANAGEMENT MEMORY
 TK = TOKEN ADDED VALUE
 L = TOKEN ADDITION INTERVAL
 P = TOKEN VALUE
 RT = LATEST SCHEDULING TIME
- ② READ FROM CACHE PORTION K = NUMBER OF PACKETS B = PACKET LENGTH SUM TOTAL valid-bit = REGISTER ENABLED AND DISABLED
- (3) INTERNAL VARIABLES
 that = IDENTIFICATION AS TO
 WHETHER TOKEN IS SUFFICIENT
 X = INSUFFICIENT TOKEN AMOUNT
 Y = NUMBER OF NECESSARY TOKEN
 ADDITION INTERVALS
 Z = NEWLY PREDETERMINED
 SCHEDULING TIME
 W = NEW TOKEN VALUE
 12bit = NEWLY PREDETERMINED
 SCHEDULING TIME
 INDICATES CROSS REFERENCE BETWEEN
 Z AND CURRENT TIME.
 Y2 = Z AND NUMBER OF TOKEN ADDITION
 ITEMS ADDED UNTIL CURRENT TIME
- OTHERS
 b = PACKET LENGTH OF PROCESSING PACKET
 NT = CURRENT TIME

FIG



ACCESS EXAMPLE OF TABLES (MANAGEMENT MEMORY, CACHE PORTION, PREDETERMINED SCHEDULING TIME MEMORY). WHEN THERE IS ONLY ONE FLOW 2 PACKET IN A PIPELINE CONCERNING THE FLOW 2 PACKET





ACCESS EXAMPLE OF TABLES (MANAGEMENT MEMORY, CACHE PORTION PREDETERMINED SCHEDULING TIME MEMORY) WHEN THERE ARE MULTINE (TWO) FLOW 2 PACKETS IN A PIEU INE CONCENNING THE FLOW 2 PACKET IN ITEM (9), THE ACCESS TO THE PROCESSING 3 REPERPORMED AT THE SAME TIME.

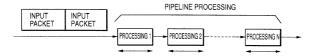


FIG.11

